

### **Amendments to the Specification**

Please replace paragraph [0029] with the following amended paragraph:

[0029] In an embodiment, the system 200 includes 36 memory devices 202 with a width W of 4 bits each and the code word has a width CW of 288 bits. When writing to the memory devices 202, the data separator 208 partitions the code word into 72 sequentially numbered nibbles of 4 bits. The data separator 208 partitions the nibbles into two ~~nibbles~~ chunks, one with even-numbered nibbles and another with odd-numbered nibbles. The buffer 204 stores the first and second chunks in respective address lines of the 36 memory devices so each pair of adjacent nibbles from the code word is stored in a different one of the 36 memory devices.